



Compound Semiconductor Based Micro (Nano) Electronics

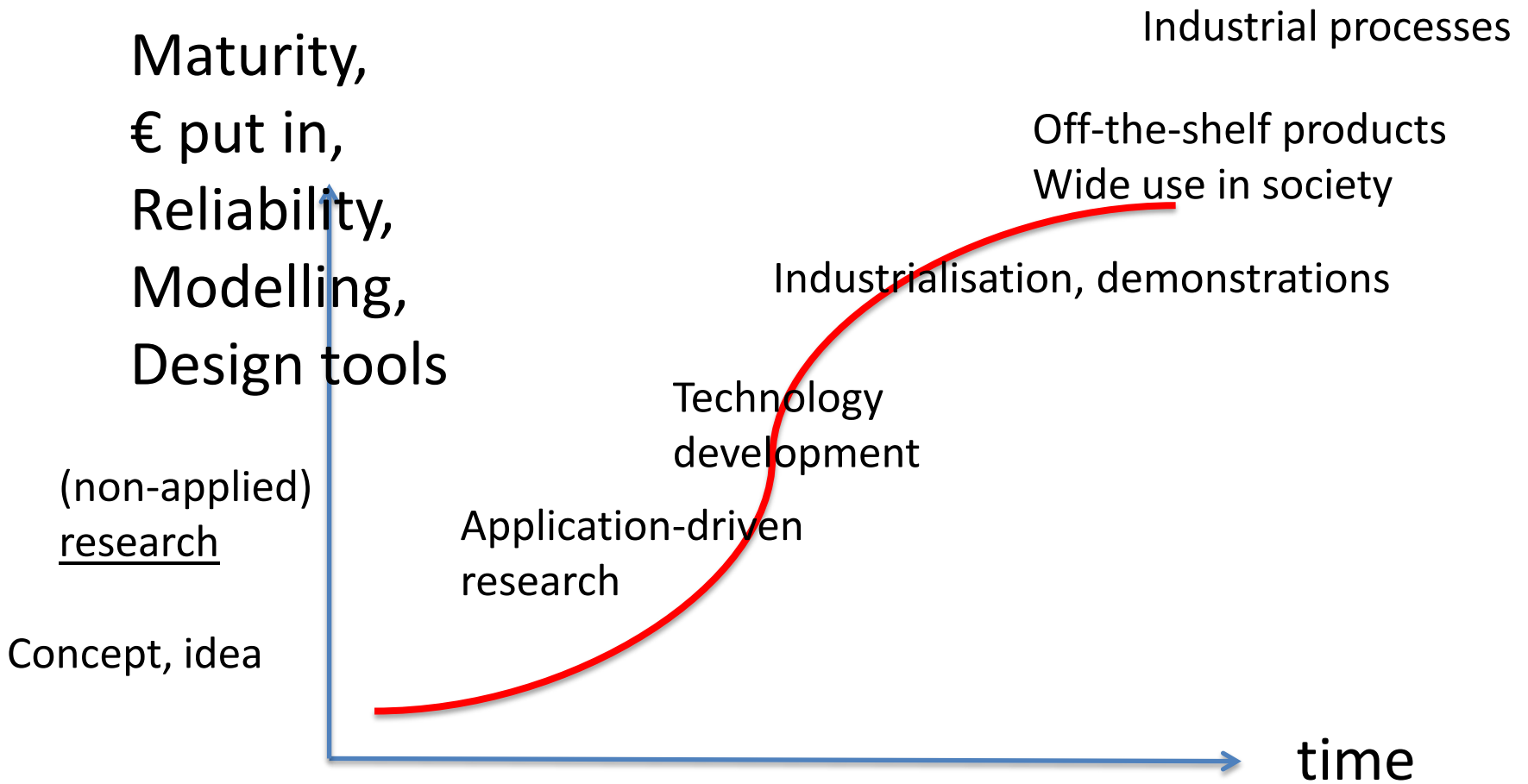
William Stanchina

Questions and Discussion

Discussant:
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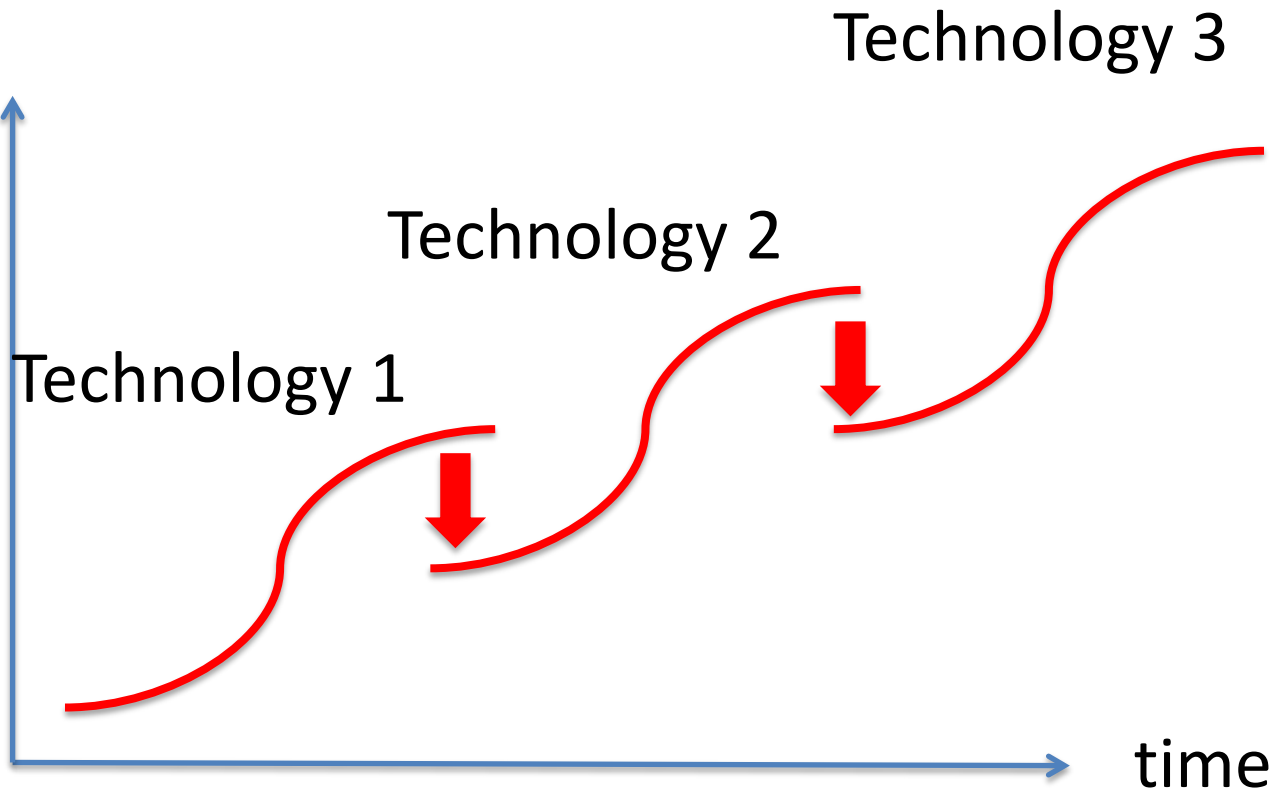
Rapporteur:
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Where are CSIC technologies at?



Where are CSIC technologies at?

Maturity,
€ put in,
reliability



Questions

- Will HEMTs and HBTs continue to be the important III-V devices – or do you foresee a renaissance for for instance tunneling-based devices?
- III-V efforts large in the US, smaller in Europe. Graphene efforts now quite large in Europe – when do we have a graphene-based IC technology?
- Interface between technology and design: accuracy of models, availability, tools/simulators,
- What drives the CSIC technologies? Applications? To stay ahead of CMOS? ...
- Cost = NRE + per-IC. Low and medium volume cost is usually different from large volume cost.

Visions – for CISC technologies

- New material system – other compounds?
- TFET?