

Compound Semiconductor Based Micro (Nano) Electronics

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Questions and Discussion

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Where are CSIC technologies at?

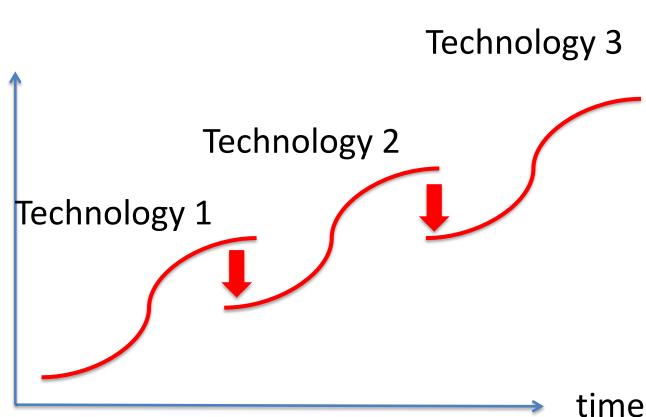
Industrial processes Maturity, € put in, Off-the-shelf products Wide use in society Reliability, Industrialisation, demonstrations Modelling, Design tools Technology development (non-applied) Application-driven <u>research</u> research Concept, idea



time

Where are CSIC technologies at?

Maturity, € put in, reliability





Questions

- Will HEMTs and HBTs continue to be the important III-V devices – or do you foresee a renecessance for for instance tunneling-based devices?
- III-V efforts large in the US, smaller in Europe.
 Graphene efforts now quite large in Europe when do we have a graphene-based IC technology?
- Interface between technology and design: accuracy of models, availability, tools/simulators,
- What drives the CSIC technologies? Applications? To stay ahead of CMOS? ...
- Cost = NRE + per-IC. Low and medium volume cost is ususally different from large volume cost.



Visions – for CISC technologies

- New material system other compounds?
- TFET?

