

# MemCo Workhop

## "Memristors for Computing"



19-21 November 2012, Fréjus, France

### AIMS

The Memco workshop aims at bringing together different communities interested in memristor devices and their applications, from condensed matter to hardware implementation. Here memristor is taken in the broadest sense, ranging from binary to multi-state memories, to be used for logic, reconfigurable or neuromorphic architectures.

### FORMAT

The workshop will consist of invited talks and poster sessions. Meals and accommodation are free of charge. Application to attend the conference shall be sent before July 31<sup>st</sup> 2012. Registration will be effective only once the candidacy has been accepted by the Scientific Committee.

### SCIENTIFIC COMMITTEE

- Julie Grollier (UMΦ CNRS/Thales, FRA)
- Olivier Temam (INRIA Saclay, FRA)
- Sylvain Saïghi (IMS, Bordeaux, FRA)
- Christian Gamrat (CEA List, FRA)
- J.-O. Klein (IEF, Orsay, FRA)
- D. Querlioz (IEF, Orsay, FRA)

### INVITED SPEAKERS

- M. Di Ventra (UC San Diego, USA)
- Y. Frégnac (UNIC, Gif s. Yvette, FRA)
- V. Garcia (CNRS/Thales, Palaiseau, FRA)
- T. Hasegawa (Nims MANA, Tsukuba, JAP)
- B. Jackson (IBM Almaden, USA)
- D. Kuzum (Stanford Univ., USA)
- B. Linares-Barranco (IMSE Sevilla, SPA)
- W. Lu (Univ. of Michigan, USA)
- T. Prodromakis (Imperial College London, UK)
- D. Querlioz (IEF Orsay, FRA)
- D. Strukov (UCSB, USA)
- S. Thorpe (CerCo, Toulouse, FRA)
- S. Williams (Hewlett Packard, Palo Alto, USA)
- D. Wright (Exeter University, UK)

Inscriptions and abstract submission deadline : July, 31<sup>st</sup> 2012

<http://www.trt.thalesgroup.com/ump-cnrs-thales/memco>