Embedded Tutorial:

"BEYOND CMOS - BENCHMARKING FOR FUTURE TECHNOLOGIES"

Moderators:
Prof Clivia M Sotomayor Torres,
Catalan Institute of Nanotechnology, Barcelona, Spain
Prof Wolfgang Rosenstiel,
edacentrum and University of Tuebingen, Germany

NANO-TEC has received funding from the European Community’s Seventh Framework Programme (FP7/2007-2013) under grant agreement n° 257964
About NANO-TEC

AIMS

- To identify the next generation of (emerging) device concepts and technologies for ICT.

- To build a joint technology-design community to coordinate research efforts in nanoelectronics.
NANO-TEC timeline

W1: Identify Technologies & Designs for new devices to work

W2: Benchmark of new Beyond-CMOS device and design concepts

W3: SWOT analysis of benchmarked devices and designs

W4: Recommendations on combined TEC-DES eco-system

January 2010
Granada

October 2011
Athens

30-31 May 2012
Lausanne

Autumn 2012

<Name>
<Short Affiliation>

Embedded Tutorial presented by the NANO-TEC Project:
"BEYOND CMOS - BENCHMARKING FOR FUTURE TECHNOLOGIES"
Presentations

• Emerging Technologies: More Moore and More than Moore
  • Dr Mart Graef, Delft University of Technology, The Netherlands

• Technology and Design challenges in future low power memory devices and circuits
  • Dr Paolo Fantini, Micron Semiconductors, Italy

• Bridging Technology and Design for Beyond CMOS
  • Prof Paolo Lugli (tentative), Technical University of Munich, Germany

• Bridging Technology and design in More than Moore
  • Dr Wladek Grabinski, EPFL, Switzerland

• Benchmarking for Beyond CMOS technologies
  • Prof Jouni Ahopelto VTT, Finland