

Embedded Tutorial:

"BEYOND CMOS - BENCHMARKING FOR FUTURE TECHNOLOGIES"

Moderators:

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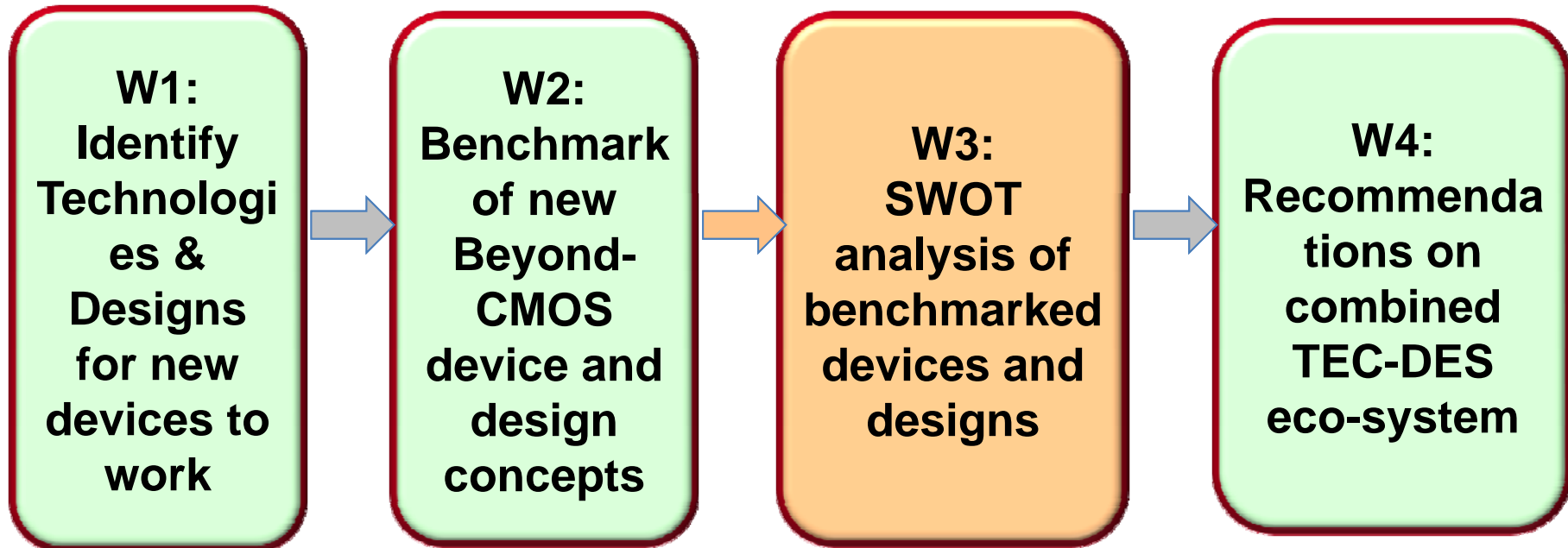
About NANO-TEC

AIMS

- ❑ To identify the next generation of (emerging) device concepts and technologies for ICT.
- ❑ To build a joint technology-design community to coordinate research efforts in nanoelectronics.



NANO-TEC timeline



January 2010
Granada

October 2011
Athens

30-31 May 2012
Lausanne

Autumn 2012

Presentations

- **Emerging Technologies: More Moore and More than Moore**
 - Dr Mart Graef, Delft University of Technology, The Netherlands
- **Technology and Design challenges in future low power memory devices and circuits**
 - Dr Paolo Fantini, Micron Semiconductors, Italy
- **Bridging Technology and Design for Beyond CMOS**
 - Prof Paolo Lugli (tentative), Technical University of Munich, Germany
- **Bridging Technology and design in More than Moore**
 - Dr Wladek Grabinski, EPFL, Switzerland
- **Benchmarking for Beyond CMOS technologies**
 - Prof Jouni Ahopelto VTT, Finland