

# Program of the final NANO-TEC Workshop: "Summary and Recommendations for the Technology-Design Ecosystem"

Wed, 2012/09/19 - 14:27 — Ralf Popp, edacentrum GmbH, DE



The final NANOTEC workshop has taken place on **November, 6-7, 2012** in **Barcelona, Spain**. The program included a series of plenary presentations on several selected technologies, discussions in several separated working groups and a panel discussion on design tools for beyond CMOS technologies.

**Location: [Hotel Front Maritim](#) [1],  
Pº García Faria, 69,  
08019 Barcelona, Spain  
Tel. +34 933 034 440**



[2] An announcing flyer of the workshop is now available [here](#) [2].

## PROGRAM


Monday, November 5, 2012	
20.00	Welcome reception at Restaurante 1881, Museum of History of Catalunya, Plaza Pau Vila 3, Barcelona
Tuesday, November 6 2012	
08.30-09.00	Registration
09.00-09.10	Welcome address: <i>Carles Cané, National Centre for Microelectronics, CSIC, Barcelona, and member of the GENESIS National Technology Platform</i>

09.10- 09.40	<p><b>Session 1:</b>  <b>Introduction and summary of Workshop 1-3,</b>  <i>Clivia Sotomayor Torres, Catalan Institute of Nanotechnology</i></p>
09.40-10.55	<p><b>Session 2:</b>  <b>Nanoelectronics in EU Horizon 2020 and SRC views</b>  <i>Dirk Beernaert, Nanoelectronics in EU Horizon 2020</i>  <i>Victor Zhirnov, SRC views on Nanoelectronics</i></p>
10.55-11.15	<p><b>Coffee break</b></p>
11.15-11.55	<p><b>Session 3:</b>  <b>Technology and Design for devices with charge as state variable</b>  <i>Mart Graef, Delft University of Technology,</i>  <i>Guilhem Larrieu, CNRS-LAAS</i></p>
11.55-12.35	<p><b>Session 4:</b>  <b>Technology and Design for devices with non charge state variables</b>  <i>Jouni Ahopelto, VTT Technical Research Centre of Finland,</i>  <i>Piotr Grabciec, ITE</i></p>
12.35- 13.15	<p><b>Session 5:</b>  <b>Technology and Design of new computing Paradigms,</b>  <i>Dag Winkler, Chalmes University, Sweden</i>  <i>Georgios Fagas, University College Cork</i></p>
13.15-14.30	<p><b>Lunch</b></p>
14.30- 15.30	<p><b>Session 6: Neuromorphic computing as a new computing paradigm,</b>  <i>Prof. Dr Simon Thorpe, CNRS</i></p>
15.30-16.00	<p><b>Coffee break</b></p>
16.00-17.30	<p><b>Parallel working groups in order to collect comments and further recommendations from the workshop participants.</b></p> <p><b>Working groups:</b></p> <p><b>Technology and Design for devices with charge as state variable</b>  <i>Convenor: Mart Graef, Delft University of Technology,</i>  <i>Rapporteur: Guilhem Larrieu, CNRS-LAAS</i></p> <p><b>Technology and Design for devices with non charge state variables</b>  <i>Convenor: Jouni Ahopelto, VTT Technical Research Centre of Finland,</i>  <i>Rapporteur: Piotr Grabciec, ITE</i></p> <p><b>Technology and Design of new computing Paradigms,</b>  <i>Convenor: Dag Winkler, Chalmes University, Sweden</i>  <i>Rapporteur: Georgios Fagas, University College Cork</i></p>

<b>20.00</b>	<b>Dinner</b>
<b>Wednesday, November 7, 2012</b>	
<b>09.00-09.45</b>	<b>Session 7: Report back from the working groups by rapporteurs</b>
<b>09.45-10.45</b>	<b>Session 8: Topological insulators</b> <i>Prof. Dr. Laurens Molenkamp, University of Wuerzburg</i>
<b>10.45-11.15</b>	<b>Coffee Break</b>
<b>11.15-11.45</b>	<b>Session 9: Ecosystem technology</b> <i>Alain Cappy, CNRS,</i> <i>Thomas Swahn, Chalmers University of Technology</i>
<b>11.45-13.00</b>	<b>Session: Panel: Design-technology integration</b>  <b>Panel Discussion: “Design Tools for Beyond CMOS technologies”</b>  <i>Chair: Livio Baldi, Micron Technology Inc., Milan;</i>  <b>Panelists:</b> <i>Wolfgang Rosenstiel, edacentrum GmbH and University of Tübingen</i> <i>Paolo Lugli, Technical University of Munich</i> <i>Sandip Tiwari, Cornell University, Ithaca</i> <i>Mustafa Badaroglu, IMEC, Leuven</i>
<b>13.00-14.00</b>	<b>Lunch</b>
<b>14.00-14.30</b>	<b>Wrapping up and concluding remarks</b> <i>Clivia Sotomayor Torres, Catalan Institute of Nanotechnology, Barcelona</i> <i>Wladek Grabinski, EPFL, Lausanne</i>

<b>14.30-15.00</b>	<b>Coffee break and end of workshop</b>

**Download:**

 [Announcement of the final NANO-TEC workshop](#) <sup>[3]</sup>

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NANO-TEC has received funding from the European Community's Seventh Framework Programme (FP7/2007-2013) under grant agreement n° 257964

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**Source URL:** <https://www.fp7-nanotec.eu/workshop4/program>

**Links:**

[1] <http://www.hotelfrontmaritim.com/en/hotel-front-maritim/>

[2] [https://www.fp7-nanotec.eu/system/files/ct\\_project\\_news/ws-flyer-nanotec-2012.pdf](https://www.fp7-nanotec.eu/system/files/ct_project_news/ws-flyer-nanotec-2012.pdf)

[3] [https://www.fp7-nanotec.eu/system/files/ct\\_project\\_news/ws-flyer-nanotec-2012\\_0.pdf](https://www.fp7-nanotec.eu/system/files/ct_project_news/ws-flyer-nanotec-2012_0.pdf)