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NANO-TEC Questionnaire on European capabilities regarding Technology-Design of the benchmarked technologies

Tue, 2012/05/29 - 18:21 — Ralf Popp, edacentrum GmbH, DE

The NANO-TEC project is working on a description of the current status of the European capabilities regarding Technology-Design of the technologies benchmarked during the NANO-TEC workshop 2. The project urgently needs your experience and opinion on that to be filled in in the questionnaire to be found below.

With clicking the submit button you submit your answers. **The poll will close on July 27, 2012**

If you have any questions you can send an email to: [workshop@fp7-nanotec \[dot\] eu](mailto:workshop@fp7-nanotec.eu).

1. Your opinion on the general 'beyond CMOS' European capabilities ...

... in modeling and design: *

... in research infrastructure tools: *

... in research infrastructure processes: *

... in research infrastructure staff: *

... in education and training : *

... for proof of concept : *

... for demonstrators: *

... for early integration in systems : *

2. Specific questions about the NANOTECH topics:

2a. Your opinion on the European capabilities in molecular electronics concerning ...

... modeling and design: *

... device and circuit processing: *

Comments explaining the answers on molecular electronics:

Please indicate information leading to your ticks on molecular electronics.

2b. Your opinion on the European capabilities in spintronics concerning ...

... modeling and design in spintronics:*

... device and circuit processing in spintronics:*

Comments explaining the answers on spintronics:

Please indicate information leading to your ticks on spintronics.

2c. Your opinion on the European capabilities in nanowires concerning ...

... modeling and design in nanowires:*

... device and circuit processing in nanowires:*

Comments explaining the answers on nanowires:

Please indicate information leading to your ticks on nanowires.

2d. Your opinion on the European capabilities in graphene concerning ...

... modeling and design in graphene:*

... device and circuit processing in graphene:*

Comments explaining the answers on graphene:

Please indicate information leading to your ticks on graphene.

2e. Your opinion on the European capabilities in MEMS concerning ...

... modeling and design in MEMS:*

... device and circuit processing in MEMS:*

Comments explaining the answers on MEMS:

Please indicate information leading to your ticks on MEMS.

2f. Your opinion on the European capabilities in solid state quantum computing concerning ...

... modeling and design in solid state quantum computing:*

... device and circuit processing in solid state quantum computing:*

Comments explaining the answers on solid state quantum computing:

Please indicate information leading to your ticks on solid state quantum computing.

2f. Your opinion on the European capabilities in neuromorphic computing concerning ...

... modeling and design in neuromorphic computing:*

... device and circuit processing in neuromorphic computing:*

Comments explaining the answers on neuromorphic computing:

Please indicate information leading to your ticks on neuromorphic computing.

3. Status of the respondent

Domain of activity: *

Organism: *

Are you partner of a FP7 project (IP, STREP, NoE...) in nanoelectronics?:

Are you partner of a FP7 project (IP, STREP, NoE...) in nanoelectronics? If yes, please tell us the name and type.

Additional information concerning European capabilities regarding Technology-Design of the benchmarked technologies:

Text you can write for the WS organisation

E-Mail::

If you are interested in the results of the poll, please insert your email adress here.

[Privacy Policy](#) ^[1]

With clicking the submit button you submit your answers to the poll.

Submit answers to poll

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Source URL: <https://www.fp7-nanotec.eu/workshop3/poll>

Links:

[1] https://www.fp7-nanotec.eu/privacy_policy