

Slides of the NANOTECH-Tutorial at ESSCIRC/ESSDERC available now

Mon, 2012/07/02 - 14:36 — Ralf Popp, edacentrum GmbH, DE

The NANO-TEC project has held a half day tutorial at the ESSDERC/ESSCIRC Conference in Bordeaux on Monday, September 17, 2012. This Tutorial has been on the **ECOSYSTEMS TECHNOLOGY and DESIGN for NANOELECTRONICS in Europe** and has presented the current outcome of the EU project NANOTECH.



The presentations of the NANOTECH ESSCIRC/ESSDERC Tutorial are available here below.

Content:

There were five different presentations starting from the general project presentation (objectives, methodology etc.) and continuing with the presentation of the outcome of NANOTECH Workshops on "Benchmarking for beyond CMOS devices" and "A swot analysis of beyond CMOS". Finally, two talks were presented on a "Case study: The nanowire transistor" and a presentation on "Information processing paradigms of the post CMOS era: devices and architectures".

The Tutorial was a successful event for dissemination of the project output. The five presentations were distributed to participants and are currently available on the project web site.

Organizers:

A.G. Nassiopoulou, NCSR Demokritos, Athens, Greece <[A \[dot\] Nassiopoulou@imel \[dot\] demokritos \[dot\] gr](mailto:Nassiopoulou@imel.demokritos.gr)>
W. Grabinski, EPFL, Lausanne, Switzerland <[wladyslaw \[dot\] grabinski@epfl \[dot\] ch](mailto:wladyslaw.grabinski@epfl.ch)>

Program Schedule

- 13:30-13:45 Welcome and Introduction
- 13:45-14:00 FP7 Project NANOTECH On Ecosystems Technology & Design For Nanoelectronics: An Experience In Europe
Androula Nassiopoulou, NCSR Demokritos/IMEL, Athens, Greece
- 14:00-14:30 Benchmarking of Beyond CMOS
Jouni Ahopelto, VTT Technical Research Centre of Finland, Helsinki, Finland
- 14:30-15:00 A SWOT Analysis of 'Beyond CMOS'
Mart Graef, DIMES, Delft Institute of Microsystems and Nanoelectronics, Delft University of Technology, The Netherlands
- 15:00-15:30 Case study: The case of nanowire transistor
Guilhem Larrieu, LAAS-CNRS, Toulouse, France
- 15:30-16:00 Coffee Break

Tutorial Aim:

This Tutorial has presented the outcome of the EU project NANOTEC. The project NANOTEC (Ecosystems Technology and Design for Nanoelectronics) is a support action funded by the European Commission under number 257964. The aim of NANO-TEC is to address the gap between technology and design in Nanoelectronics in the area of Beyond CMOS. Within the scope of NANOTEC, a number of workshops have been organized, where specific 'Beyond CMOS' device concepts were presented and discussed by experts in the field. Some categories of beyond CMOS devices were selected, that were benchmarked and a SWOT analysis was carried out. To this end NANOTEC carries out a continued consultation and analysis of research needs and trends based on a workshop series with invited experts from the Americas, Asia and Europe covering topics such as Beyond CMOS device concepts and design, benchmarking and a SWOT analysis of new devices. Progress made so far towards the stated aims will be presented in this tutorial by the different speakers.



Download:

-  [Announcement of the Tutorial as a PDF](#) ^[1]
-  [Program of the Tutorial as a PDF](#) ^[2]
-  [Slides of the Tutorial as a ZIP of PDFs](#) ^[3]

NANO-TEC has received funding from the European Community's Seventh Framework Programme (FP7/2007-2013) under grant agreement n° 257964

Source URL: <https://www.fp7-nanotec.eu/esscirc-essderc-tutorial>

Links:

- [1] https://www.fp7-nanotec.eu/system/files/ct_project_news/announcement_nanotec_tutorial_in_essderc_esscirc_2012__0.pdf
- [2] https://www.fp7-nanotec.eu/system/files/ct_project_news/nanotec_tutorial_in_essderc-esscirc_program_abstracts.pdf
- [3] https://www.fp7-nanotec.eu/system/files/ct_project_news/nano-tec-at-esscirc_essderc.zip