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Welcome to NANO-TEC

NANO-TEC seeks to build a community of academic researchers in nanoelectronics, addressing specifically research in Beyond CMOS from the combined technology and design perspectives. A methodology for continued consultation and analysis of research needs and trends will be developed. The main activity will be a workshop series with invited experts, preceded by a methodology-contents preparation phase and subsequent analysis and documentation, both by the consortium. Apart of determining what is relevant for Beyond CMOS devices and design, benchmarking and SWOT analyses will be performed. An end-of-the-project public dissemination event will present the results of the work of NANO-TEC to stake-holders, including the EC and relevant ETPs.

Two elements are crucial here. One is the access to the huge expertise in Europe, albeit still fragmented, in the area of Beyond CMOS both in technology and in design. The other is a platform to carry out the work and document it. The former is inherent to the consortium, although non-exclusive, as partners come mainly from institutions which have a tradition of nanofabrication for nanoelectronics research and or are members of national and international consortia and as such have contacts to leading researcher in Beyond CMOS Nanoelectronics. The web-based work platform is part of the long-term community-building aim and will enable documents and exchanges to take place, as well as to be the place where the working groups can evolve into a Specialist Interest Group on the combined ecosystems of technology and design.

Keywords

Nanoelectronics, beyond CMOS devices and designs, community building, benchmark, combined design and technology.

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